

TECHNICAL DATASHEET

AVBR00810H49

The AVBR00810H49 is a 80W high gain Solid State Broadband High Power Amplifier. This amplifier module utilizes the latest high power RF LDMOS transistors and also features built in control and monitoring, with protection functions to ensure high availability. This amplifier is suitable for broadband jamming and EMC testing.

Features

80MHz-1000MHz frequency range	Solid-state Class AB Broadband design
Psat 50dBm type	Instantaneous ultra-broadband
Power gain 50dB	Suitable for AM, and FM
50 ohm input/output impedance	Small and lightweight
Built-in control, monitoring and protection circuits	High reliability and ruggedness

ELECTRICAL SPECIFICATIONS(T=25°C,DC Voltage= 28V,Load VSWR ≤ 1.2)

Description	Symbol	Min	Typ	Max	Unit
Operating Frequency	BW	80		1000	MHz
Output Power CW	Psat	65	80		W
Power Gain @ Psat	Gp	49	50		dB
Power Gain Flatness @ Rated PSAT	ΔGp		± 2	± 2.6	dB
Input Power for Rated PSAT	P _{IN}		0		dBm
Harmonics @ Pout = 50W	2 nd		-15	-13	dBc
Noise Figure	NF		12		dB
Spurious Signals@ Pout =20W	Spur		-60		dBc
Input Return Loss	S11			-10	dB
Operating Voltage	VDC	26	28	30	V
Current Consumption @ Pout= 80W	IDD		9.5	11.5	A
Switching Time @ 1kHz TTL, P _{IN} =0dBm	TON/TOFF		2	5	μs

MECHANICAL SPECIFICATIONS

Cooling External HeatSink Needed (Not Supplied)	
Length* Width*Height [mm]	150*90*25
Weight [Kg]	0.6
RF Connector Input	SMA, Female
RF Connector Output	SMA, Female

ENVIRONMENTAL SPECIFICATIONS (Design to Meet)

Module Operation Temperature	-20	60	°C
Storage Temperature Range	-25	65	°C
Relative-Humidity	N/A		
Altitude	N/A		
Vibration/Shock	N/A		

LIMITS

Input RF drive level without damage	Pin ≤ 10	dBm
Load VSWR @ POUT = 50W	VSWR ≤ 5:1 [Design To Meet]	N/A
Load VSWR @ POUT = 80W	VSWR ≤ 3:1 [Design To Meet]	N/A
Thermal Degradation	90°C @ heatsink [recovery@ 60°C]	°C

DC INTERFACE CONNECTOR – [Hybrid D-Sub 7W2, Male]

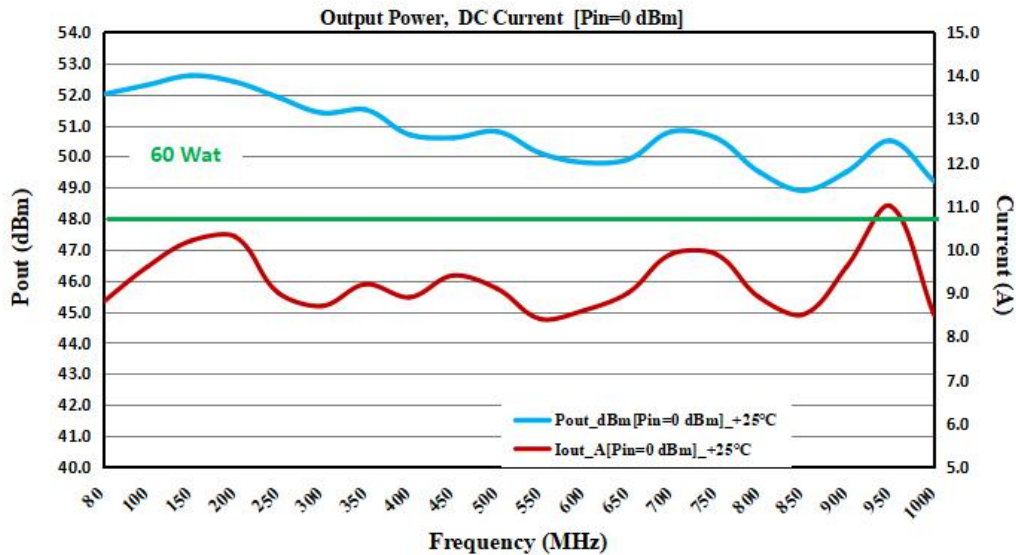
Pin #	Description	Specifications
A1	GND	Ground
A2	VDD	28VDC
1	CURRENT SENSE	Analog voltage relative to IDD @ 100mV per Ampere
2	TEMP SENSE	Analog voltage relative to Module' s Temperature @ 10 mV/° C
3	ENABLE	Amplifier Enable: TTL Logic High (3.3V) (Internally)
4	GND	Ground
5	N/C	No Connection

PLOTTED AND OTHER DATA

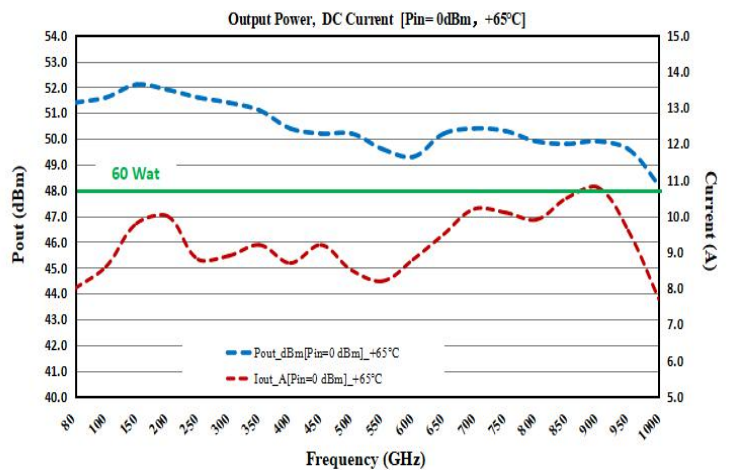
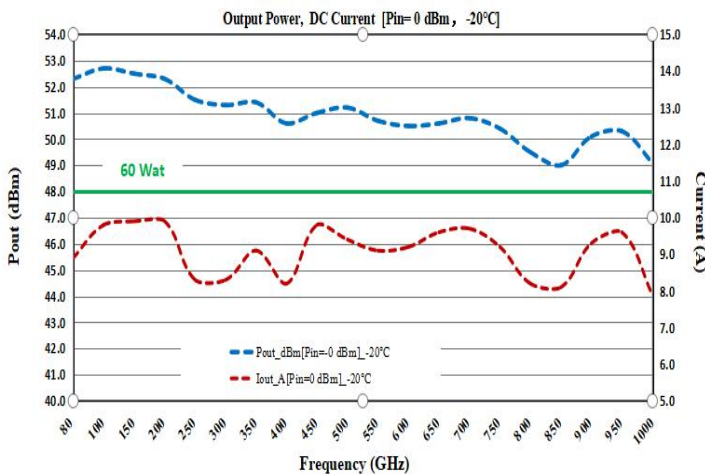
Notes:

1. Values at +25°C, sea level.
2. ESD Sensitive Material, Transport material in Approved ESD bags. Handle only in approved ESD Workstation.
3. Heat Sink required for Proper Operation, Unit is cooled by conduction to heat sink.

TYPICAL PERFORMANCE DATA [Load VSWR ≤ 1.2],(Normal temp. +25±3 °C)



Graph1: Output Power (Low temp. -20±3 °C & High temp. +60±3 °C)



OUTLINE DRAWING [mm]

