SE1022-DSP Lock-In Amplifier 1 mHz to 102 kHz





Features

- 1 mHz to 102 kHz frequency range
- 1 nV to 1 V full-scale sensitivity
- Time constants from 10 µs to 3 ks
- >120 dB dynamic reserve
- Automatic adjustment
- Multiple-harmonic measurement
- FFT spectral analysis
- 5.6 inch color TFT-LCD screen

Overview

SE1022 Digital Lock-in Amplifier provides an excellent performance within its bandwidth from 1 mHz to 102 kHz. With the advantage of the latest digital signal processing technology and high-precision 24-bit ADC, SE1022 can easily detect the phase and the magnitude of weak signals overwhelmed by various large noise. The performance of SE1022 is as good as other lock-in amplifiers all over the world, even better than them in some certain parameters, such as measurement accuracy, SNR, dynamic reserve. Otherwise, SE1022 integrates some special functions like multiple harmonic measurement and FFT, which meets the needs of scientific research and industrial application well.

Input Channel

SE1022 detects an input signal in a single-ended mode or a differential voltage mode. With an ultra low-noise pre-amplifier, the input noise is as low as 6 nV/ $\sqrt{\text{Hz}}$ @997 Hz. The input impedance is 10 M Ω and the full-scale input voltage sensitivity ranges from 1 nV to 1 V. Besides, SE1022 can be used for current measurement with gains of 10^6 or 10^8 V/A. Two line filters (50/60 Hz and 100/120 Hz) are designed to eliminate power frequency interference. A programmable gain amplifier is used to adjust the dynamic reserve of the system, so that SE1022 can keep a high dynamic reserve of 100 dB. The high-precision 24-bit ADC has a sampling rate of 312.5kSPS, and the excellent anti-aliasing filter in front of the ADC can effectively prevent signal aliasing.



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Reference Channel

The reference signal can work in external mode or internal mode. In internal mode, a precise and stable internal oscillator generates sine wave as an internal reference that is multiplied by the input signal. This internal signal is without any phase noise. With the digital phase-shifting technique, the phase resolution of the reference signal is 0.01°. SE1022 can work at any fixed frequency from 1 mHz to 102 kHz in this mode. In external mode, the reference signal can be a sine wave or a TTL pulse or a square wave. The rising or falling edge of the external reference signal triggers the Phase Lock Loop (PLL) to lock the external signal. Based on the frequency of the reference signal, the SE1022 can detect the harmonics of the input signal. The maximum harmonic signal frequency can reach 32,767 times the fundamental frequency, and the maximum harmonic frequency cannot exceed the maximum operating frequency of the instrument by 102 kHz.

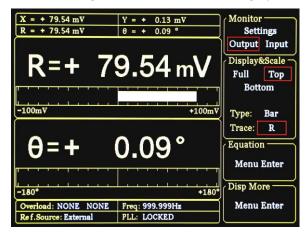
Digital Demodulator and Output Filter

The key component of the SE1022 is the digital demodulator. Compared to traditional analog lock-in amplifiers, the SE1022's internal digital demodulator effectively rejects the measurement errors caused by DC drift and offset. In addition, by optimizing the multiplication of the internal

coherent signal of the digital demodulator, the calculation error is minimized so that the instrument can accurately detect the input weak signal. Time constants of the output low-pass filter from 10 µs to 3 ks can be selected with a choice of 6, 12, 18 or 24dB/oct rolloff. This low-pass digital filter is implemented using a high performance digital filter with a sample rate of 312.5 kHz. The digital demodulation and the low-pass filter used in SE1022 guarantees a high dynamic reserve (>100dB), accurate phase (absolute phase error <1°). Moreover, when the frequency of the input signal is lower than 200 Hz, A synchronous filter can be used to eliminate the influence of the harmonics of the reference signal, ensuring that SE1022 can detect a low-frequency signal quickly and effectively.

Display

SE1022 has a 5.6-inch 640 × 480 color TFT-LCD. The measurement results of SE1022, such as X, Y, R, and θ , are shown in numerical form, bar graph, X-Y chart and polar coordinates on the display.

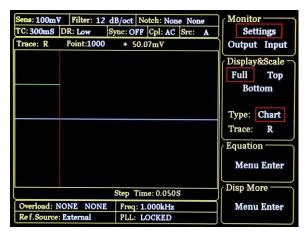




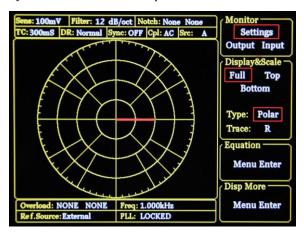
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In X-Y chart, SE1022 shows the trend of test results over time, and check the value by using knob control cursor.



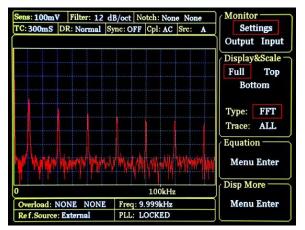
In addition, SE1022 can also uses polar coordinates to display the in-phase component and quadrature component of the input signal. All of these display modes can be easily adjusted by manual or automatic operations.



FFT Spectral Analysis

SE1022 integrates a high precision FFT analysis

function from 1 mHz to 102 kHz in order to analyze the noise component of the measured signal in real time.



Simultaneous Multiple-harmonic Measurement

In the traditional lock-in amplifiers, only the fundamental frequency signal or a certain harmonic signal can be measured at one time, so it cannot meet the requirement of multipleharmonic measurement in some occasions. On the contrary, SE1022 uses a flexible digital framework combined FPGA and ARM, which make it practicable and efficient to measure 3 harmonic components simultaneously, which means that one SE1022 is equivalent to three traditional lock-in amplifiers. The maximum harmonic signal frequency can reach 32,767 times the fundamental frequency, but the maximum harmonic frequency cannot exceed the maximum operating frequency of the instrument by 102 kHz.



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C: 300mS DR: Normal S	ync: OFF Cpl: AC Src: A	Settings
X = + 71.74 mV	Y = -1.21 mV	Output Input
R = + 71.75 mV	$\theta = - 0.97$ °	Display&Scale
Xh1 = + 71.74 mV Rh1 = + 71.75 mV	Yh1 = - 1.22 mV 0h1 = - 0.97 °	Bottom
Xh2 = + 1.37 mV Rh2 = + 23.85 mV	Yh2 = + 23.81 mV 0h2 = + 86.70 °	Type: List Trace: ALL
AD1 = + 0.000V	AD2 = + 2.033V	Equation —
AD3 = + 0.000V DA1 = + 0.000V	AD4 = + 2.052V DA2 = + 0.000V	Menu Enter
DA3 = + 0.000V	DA4 = + 0.000V	Disp More —
Overload: NONE NONE Freq: 299098882		Menu Enter

Internal Oscillator

The internal oscillator of SE1022 generates a low distortion (-80 dBc) sine reference signal varying from 1 mHz to 102 kHz, which has a high frequency resolution of 1 mHz. The frequency and amplitude of the reference signal can be set by using the front panel of SE1022 or communication interface. When SE1022 is set in the external reference mode, the internal reference signal is phase-locked with the external reference signal.

Signal Generator

SE1022 uses a high precision digital-to-analog converter (DAC) to output a sine wave signal at the same frequency as the internal reference signal. The amplitude and phase of the output sine wave can be set through the SE1022's display, where the maximum amplitude of the sine wave is 5 Vrms.

Manual Operation

The parameters are convenient to be adjusted by the soft keys besides the display and the numeric keypad on the front panel, such as the internal oscillator frequency and the SINE OUT amplitude.

Auto Function

SE1022 can automatically adjust itself into different optimal operating modes for different input signals, such as Auto Gain mode, Auto Reserve mode and Auto Phase mode. This function makes it easier for users to measure signals more efficiently.

Interface

SE1022 uses RS-232 to USB interface as standard. GPIB interface is also provided as an optional interface. Through communication interfaces, all instrument functions can be controlled and all data can be read in real-time. Meanwhile, all interfaces of SE1022 are distributed on the front panel and the rear panel.

Remote Operation

Users can use PC to control SE1022 through communication interfaces, including setting the parameters and reading the measurement data. SE1022 is equipped with a free LabVIEW program, which makes it easy to use in complex scientific experiments.



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Connect		INPUT OVLD			Model SE1	
USB Resource		ADC OVLD			DSP Lock-in	
N 🔳		PLL LOCKED			Control Con	sole
ut Option Output & Sam	ple Harmonic Waveform	() ()	RXYØ	Frequency X-N	oise	Clear Waveform
Input Source	Reference Phase (")	Sweep Start Freq(Hz)	1.			
Single-Ended v	0.00	1000.000	0.8			
Current Gain	Reference Source	Sweep Stop Freq(Hz)	0.6			
1MO Gain	External	5000.000	0.4			
Grounding Mode	External Ref Trigger	Sweep Step Time(ms)	0.000			
Float v	TTL Rising Edge 🚽	1000	0.2			
Input Coupling	Internal Frequency (Hz)	Sweep Step Freq(Hz)	0.3			500
AC V	1000.000	1000.000	0			500
Input Notch Filter	Sweep Type	Sweep Run Mode	R (V)	X (V)	2002	
None 🗸	Linear 🗸	Stop 🗸	R (V) 0.000	X (V)	Y (V)	Save Data
E Contraction of the Contraction			0.000	0.000	0.000	Save Data
Configure Input	Auto Phase	Configure Reference	e (')	Freq (Hz)	Noise (V)	Data Rate(S)
			0.000	0	0.000	. 0.1
Harm.1 Harm.2	Dynamic Reserve	Time Constant	Rh1 (V)	Xh1 (V)	Yh1 (V)	eh1 (*)
1 1 1	Normal 👳	300 ms 🗸	0.000	0.000	0.000	0.00
Configure Harm.	Sensitivity	Filter Slope				
configure name.	100 mV/nA	12 dB/oct v	Rh2 (V)	Xh2 (V)	Yh2 (V)	9h2 (')
			0.000	0.000	0.000	0.00
Auto Reserve	Configure	Sync Filter Off	AUX-ADC1 (V)	AUX-ADC2 (V)	AUX-ADC3 (V)	AUX-ADC4 (V)
Auto Sensitivity	Sensitivity and Reserve	Configure Filter	0.000	0.000	0.000	0.000

Technical Specifications

> Signal Channel

Voltage Input Mode Full-scale Sensitivity	Single-ended or Differential 1 nV to 1 V in a 1-2-5 sequence 1 fA to 1 μA	Input Frequency range Reference input Input impedance	1 mHz to 102 kHz TTL or Sine 1 MΩ//25 pF
Current Input Impedance Voltage Current C.M.R.R	10 ⁶ or 10 ⁸ V/A 10 M Ω // 25 pF, AC or DC coupled 1 k Ω to virtual ground >100 dB to 10 kHz,	Phase Resolution Absolute phase error Relative phase error Orthogonality Phase noise Internal ref.	0.01° <1° <0.01° 90°±0.001° Synthesized, <0.0001°rms
Dynamic Reserve Gain Accuracy Voltage Noise Current Noise Line Filters	0.2% typ, 1% max 5 nV/√Hz at 997 Hz 15 fA/√Hz at 97 Hz 13 fA/√Hz at 997 Hz 50/60 Hz and 100/120 Hz	External ref. Drift Harmonic Detection	at 1 kHz 0.001°rms at 1 kHz (100 ms time constant, 12 dB/oct) <0.01°/°C below 10 kHz <0.1°/°C above 10 kHz 2F, 3F,nF to 102 kHz (n<32767)
Gounding	BNC shield can be grounded or floated via $10 \ k\Omega$ to ground	Acquisition Time Internal ref. External ref.	Instantaneous acquisition (2 cycles + 5 ms) or 40 ms, whichever is larger

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Reference Channel



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> Demodulator

Stability

Digital output	no zero drift on all setting
Display	no zero drift on all setting
Analog output	<5 ppm/ $^{\circ}\!\!\mathbb{C}$ for all dynamic
	reserve settings
Harmonic Rejection	-90 dB
Time Constant	10 µs to 3 ks (<200 Hz)
	10 µs to 30 s (>200 Hz)
	(6, 12, 18, 24 dB/oct
	rolloff)
Synchronous Filters	Available below 200 Hz
	(18, 24 dB/oct rolloff)

Internal Oscillator

Frequency 1 mHz to 102 kHz Range Accuracy 2 ppm + 10 µHz Resolution 1 mHz Distortion -80 dBc (f<10 kHz), -70 dBc (f>10 kHz) Amplitude 0.001 to 5 Vrms Accuracy 1% Stability $50 \text{ ppm/}^{\circ}\text{C}$ Output Sine output on front panel TTL sync output on rear panel

Interfaces

RS-232 to USB interface, and IEEE-488 interface (optional).

> Display

Screen	5.6 inch, 640×480 TFT
Screen Format	Single or dual display
Display Quantities	Each display shows one trace,
	traces can be defined as X,Y,R, θ
Display Types	Numerical form, bar graph, polar
	plot and strip chart

> Outputs

CH1 and CH2 Output	S
Function	Output X, Y, R, θ
Output voltage	± 10 V full scale,
	30 mA max output current
Update rate	312.5 kHz

> General

Power Requirement	
Voltage	220 - 240 VAC,
	100 - 120 VAC (optional)
Frequency	50/60 Hz
Power	30 W
Dimension	448 (W)×148 (H)×513 (D) mm
	(with feet)
Weight	11kg

